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PATENT APPLICATION
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Masaru FUKU, et al.

Appln. No. 09/419,787

Group Art Unit: 2822

Confirmation No.: Not Yet Assigned

Examiner: J. Mitchell

Filed: October 18, 1999

For: ONBOARD SEMICONDUCTOR DEVICE

EXCESS CLAIM FEE PAYMENT LETTER

Commissioner for Patents
Washington, D.C. 20231

Sir:

An Amendment Under 37 C.F.R. § 1.111 is attached hereto for concurrent filing in the above-identified application. The resulting excess claim fee has been calculated as shown below:

	After Amendment		Highest No. Previously Paid For						
All Claims	5	-	20	=		X	\$18.00	=	\$0.00
Independent	5	-	3	=	2	X	\$80.00	=	\$160.00
TOTAL									= \$160.00

Please charge the statutory fee of \$160.00 and any additional fee or credit any overpayment to our

Deposit Account No. 19-4880. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Cameron W. Beddard
Cameron W. Beddard
Registration No. 46,545

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Date: July 3, 2001



PATENT APPLICATION

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#77A
F. BELL
7/12/01

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AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

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10-2-00 MAIL ROOM

Sir:

In response to the Office Action dated April 12, 2001 please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claim 1 without prejudice or disclaimer.

Please enter the following amended claims:

2. (Amended) An onboard semiconductor device comprising:

a power chip circuit board on which a power chip is mounted;

a control circuit board having mounted thereon an electrical component in relation to said

power chip;

an outer case in which said power chip circuit board and the control circuit board are

contained; and

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